

MicroNote 130

Overall Selection of Transient Voltage Suppressor Part Numbers

By Kent Walters

MicroNotes 104, 125, and 127 have described how to calculate and select transient voltage suppressor (TVS) devices when knowing the waveform, the source impedance (Z_s), and the open-circuit voltage (V_{oc}) of the transient. This also involves considering different peak pulse power (P_{pp}) capabilities for various pulse widths beyond those specifically rated in TVS datasheets (such as 10/1000 μ s). Additional methods will be demonstrated to calculate and summarize all Microsemi TVS part numbers compliant to the RTCA/DO-160G specification for “Environmental Conditions and Test Procedures for Airborne Equipment.” One of the fundamental relations in selecting the TVS is identifying the resulting surge or pulse current (I_P) with the aforementioned features. This has previously been shown as:

$$I_P = (V_{oc} - V_c) / Z_s$$

With this example, we can also determine the resulting P_{pp} , by multiplying the I_P value by the V_c . For this worst-case calculation, the V_c is considered the maximum value, and I_P becomes the peak pulse current (I_{pp}) at the pulse width and waveform of interest. We then have:

Equation 1:

$$(V_c)(I_{pp}) = P_{pp} = V_c (V_{oc} - V_c) / Z_s$$

Emphasizing the power instead of current simplifies matters when gaining insight to various TVS products with specific P_{pp} ratings. It also gives opportunity to examine a large number of products and their specifications, including information on V_{oc} and Z_s when defining the transient pulse. In this analysis, the P_{pp} is the determined value TVS at the desired waveform and pulse duration, after a conversion is made from the original datasheet ratings (such as 10/1000 μ s). These methods for conversion are also described in MicroNotes 104, 120, and 127. When using the appropriate P_{pp} values, we can then solve for V_c and what values comply with the necessary P_{pp} after these conversions are made.

When rearranging terms in Equation 1, we have:

$$Z_s P_{pp} = V_c V_{oc} - V_c^2$$

or,

$$V_c^2 - V_{oc} V_c + Z_s P_{pp} = 0$$

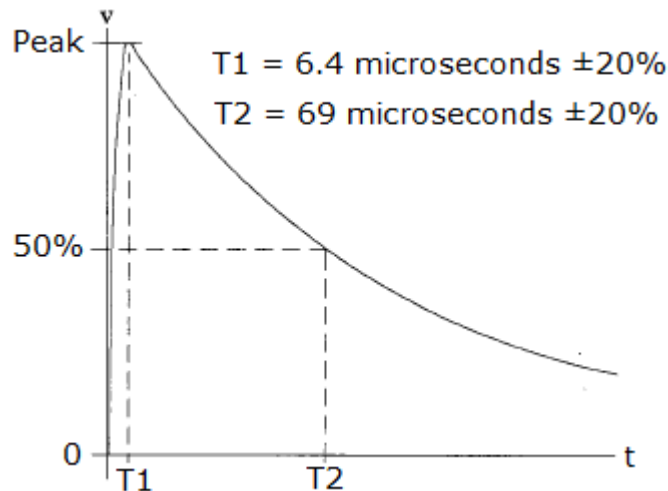
This is in a format for easily solving V_c using the quadratic equation. When using that method, we have:

Equation 2:

$$V_c = 0.5(V_{oc}) \pm 0.5(V_{oc}^2 - 4Z_s P_{pp})^{1/2}$$

This V_c calculation provides an easy way to recognize what clamping voltages are required for each of the popular TVS ratings, in order to comply with various industry specifications (such as the RTCA/DO-160G for aircraft lightning). For example, we can use the RTCA/DO-160G, Section 22, and Table 22-2 concerning “Test Levels for Pin Injection” and its described Z_s and V_{oc} values, to determine what V_c values will comply. We can use Equation 2 for various power ratings of TVSs after converting the P_{pp} to the desired waveform. For waveform 4 (6.4/69 μ s), MicroNote 127 describes a P_{pp} conversion factor of 3.33 from the longer duration 10/1000 μ s rating used in the industry. This conversion factor includes an added 20% worst-case condition in duration (6.4/83 μ s), as also required in the RTCA/DO-160G specification in “Environmental Conditions and Test Procedures for Airborne Equipment.”

Figure 1: RTCA/DO-160 Voltage Waveform 4



If we use a TVS rated at 500 W at 10/1000 μ s, this equates to a P_{PP} level of $3.33 \times 500 = 1665$ W for the described waveform 4. When also using the V_{OC} and Z_s values for waveform 4 from Table 22-2 of RTCA/DO-160G, we can solve equation for V_c . For all five levels of waveform 4, the generator source impedance is 5Ω (V_{OC}/I_{SC}). Starting with level 1 or level 2 conditions with their low V_{OC} values and substituting into Equation 2, we find that the roots of the equation are both imaginary numbers, as there is a square root of a negative value. This indicates that the V_{OC} for level 1 and 2 are comparatively low (50 V and 125 V, respectively) relative to the product of source impedance Z_s and peak pulse power P_{PP} . However this is not the case for level 3 or higher. Level 3 specifies a V_{OC} of 300 V. Substituting the V_{OC} value of 300 V from Table 22-2 with a Z_s of 5Ω , as well as the earlier determined P_{PP} value of 1665 W as the equivalent capability for the shorter waveform 4, we have the following:

$$V_c = 0.5 (300) \pm 0.5 [300^2 - 4 (5)(1665)]^{1/2}$$

$$V_c = 150 \pm 0.5 (90,000 - 33,300)^{1/2} = 150 \pm 119$$

Therefore:

$$V_c = 31.0 \text{ V and } V_c = 269 \text{ V}$$

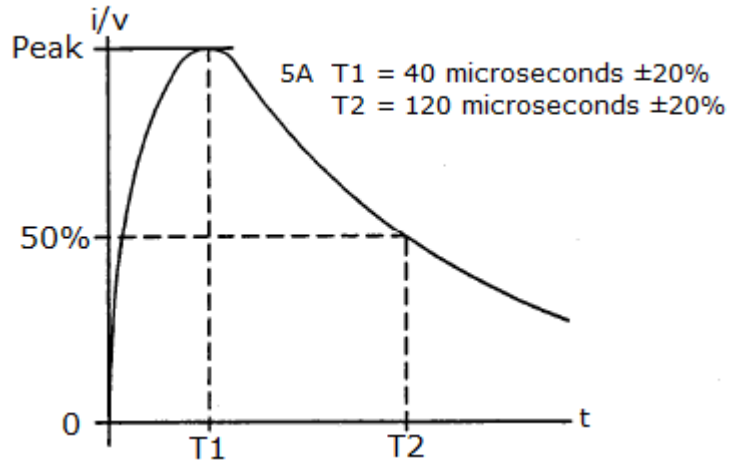
When further comparing these V_c values with Equation 1, the P_{PP} will be less than or equal to 1665 W at 6.4/83 μ s (500 W at 10/1000 μ s) when $V_c \leq 31.0$ V or $V_c \geq 269$ V. As a result, the TVS device will also meet the requirements of the DO-160D specification for pin injection tests of waveform 4 at level 3 conditions at 25 °C when the V_c is in these lower or higher specified ranges. If the 500 W series of TVSs does not have the higher voltage devices included in that series where $V_c \geq 269$ V, only the lower voltage device part numbers will comply where $V_c \leq 31.0$. For example, in the 500 W rated TVSs of the SMAJ5.0A through SMAJ170A (or CA) series, only the SMAJ5.0A through SMAJ18A (or CA) may be used for waveform 4, level 3 after comparing the maximum clamping voltage V_c values specified in the datasheet for each device in this particular series.

A similar analysis for the more severe level 4—with a higher V_{OC} of 750 V—would reveal that the clamping voltages must be $V_c \leq 11.3$ V or $V_c \geq 738.7$ V. This only allows the lowest TVS part numbers in the 500 W SMAJxxx series to be used such as the SMAJ5.0A, SMAJ6.0A, and SMAJ6.5A (or CA). The clamping voltage devices above 738.7 V have virtually no practical application and do not exist in this TVS series.

An analysis of level 5 of waveform 4 would reveal that no devices in the standard 500 W rated TVS products at 10/1000 μ s would comply with that test level threat. As a result, higher P_{PP} ratings for TVSs must be used.

The same calculations can be made for other popular TVS devices from Microsemi with P_{PP} ratings up to 200,000 W. These are all summarized in the tables shown herein for important waveforms and threat levels in the RTCA/DO-160G specification. This includes waveform 5A (40/120 μ s as shown in the following illustration), which is much more severe with its longer duration and lower source impedance.

Figure 2: RTCA/DO-160 Voltage Waveform 5A



Although three waveforms are specified in table 22-2 of the RTCA/DO-160G specification (waveforms 3, 4, and 5A), MicroNote 127 states that any TVS that complies with the frequently specified waveform 4 will also easily comply with the shorter waveform 3. Therefore, the waveform 3 calculations are not shown. All the TVS part numbers relative to their V_c calculation in Equation 2 have been determined and listed for compliance at 25 °C to the worst-case conditions of waveform 4 and waveform 5A. See the following tables for all five threat levels identified in the RTCA/DO-160G specification. For higher temperature deratings, see MicroNote 132.

Table 1: Waveform 4 Clamping Voltage and Microsemi TVS Part Numbers Compliant to RTCA/DO-160G at 25 °C

P _{PP} @10/1000 μs (or as specified)	LEVEL 1	LEVEL 2	LEVEL 3	LEVEL 4	LEVEL 5
500 W	All SMAJ5.0A-170A,CA P5KE5.0A-170A,CA 1N6103A-6137A 1N6461-1N6468 HSMBJSAC5.0-50 SAC5.0-50	All SMAJ5.0A-170A,CA P5KE5.0A-170A,CA 1N6103A-6137A 1N6461-1N6468 HSMBJSAC5.0-50 SAC5.0-50	V _C ≤ 31.0 V SMAJ5.0A-18A,CA P5KE5.0A-18A,CA 1N6103A-6114A 1N6461-1N6464 HSMBJSAC5.0-15 SAC5.0-15	V _C ≤ 11.3 V SMAJ5.0-6.5A,CA P5KE5.0-6.5A,CA 1N6103A 1N6461-1N6462	None
600 W	All SMBJ5.0A-170A,CA P6KE6.8A-200A,CA	All SMBJ5.0A-170A,CA P6KE6.8A-200A,CA	V _C ≤ 38.2 V SMBJ5.0A-22A,CA P6KE6.8A-27A,CA	V _C ≤ 13.6 V SMBJ5.0-8.0A,CA P6KE6.8A-9.1A,CA	None
1500 W	All SMCJ5.0A-170A,CA 1.5KE6.8A-400A,CA 1N5629A-1N5665A 1N5907, 1N5908 1N6036A-1N6072A 1N6138A-1N6173A 1N6267A-1N6303A 1N6469-1N6476 LC6.5-170A LCE6.5-170A SMCJLCE6.5-170A	All SMCJ5.0A-170A,CA 1.5KE6.8A-400A,CA 1N5629A-1N5665A 1N5907, 1N5908 1N6036A-1N6072A 1N6138A-1N6173A 1N6267A-1N6303A 1N6469-1N6476 LC6.5-170A LCE6.5-170A SMCJLCE6.5-170A	All SMCJ5.0A-170A,CA 1.5KE6.8A-400A,CA 1N5629A-1N5665A 1N5907, 1N5908 1N6036A-1N6072A 1N6138A-1N6173A 1N6267A-1N6303A 1N6469-1N6476 LC6.5-170A LCE6.5-170A SMCJLCE6.5-170A	V _C ≤ 35.0 V SMCJ5.0A-20A,CA 1.5KE6.8A-24A,CA 1N5629A-1N5642A 1N5907, 1N5908 1N6036A-1N6048A 1N6138A-1N6151A 1N6267A-1N6280A 1N6469-1N6472 LC6.5-20A LCE6.5-20A SMCJLCE6.5-20A	V _C ≤ 16.0 V SMCJ5.0A-8.0A,CA 1.5KE6.8A-11A,CA 1N5629A-1N5634A 1N5907, 1N5908 1N6036A-1N6040A 1N6138A-1N6143A 1N6267-1N6272A 1N6469-1N6470 LC6.5-9.0A LCE6.5-9.0A SMCJLCE6.5-9.0A
3000 W	All SMLJ5.0A-170A,CA	All SMLJ5.0A-170A,CA	All SMLJ5.0A-170A,CA	V _C ≤ 74.0 V SMLJ5.0A-45A,CA	V _C ≤ 32.0 V SMLJ5.0A-18A,CA
5000 W	All 5KP5.0A-110A,CA	All 5KP5.0A-110A,CA	All 5KP5.0A-110A,CA	V _C ≤ 135.5 V 5KP5.0A-78A,CA	V _C ≤ 54.0 V 5KP5.0A-33A,CA
15,000 W	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA	V _C ≤ 175.5 V 15KP17A-100A,CA PLAD15KP5.0-100A,CA
30,000 W	All 30KPA28A-288A,CA PLAD30KP10-400A,CA*	All 30KPA28A-288A,CA PLAD30KP10-400A,CA*	All 30KPA28A-288A,CA PLAD30KP10-400A,CA*	All 30KPA28A-288A,CA PLAD30KP10-400A,CA*	V _C ≤ 426 V 30KP33A-260A,CA PLAD30KP10-260A,CA*
65,000 W @ 6.4/69 μs	All RT65KP48-75A,CA	All RT65KP48-75A,CA	All RT65KP48-75A,CA	All RT65KP48-75A,CA	All RT65KP48-75A,CA
100,000 W @ 6.4/69 μs	All RT100KP40-400A,CA	All RT100KP40-400A,CA	All RT100KP40-400A,CA	All RT100KP40-400A,CA	V _C ≤ 426 V RT100KP40-200A,CA
130,000 W @ 6.4/69 μs	All RT130KP275-295CA, RT130KP275-295CV**	All RT130KP275-295CA, RT130KP275-295CV**	All RT130KP275-295CA, RT130KP275-295CV**	All RT130KP275-295CA, RT130KP275-295CV**	All RT130KP275-295CA, RT130KP275-295CV**

1. The CA suffix signifies bidirectional TVS options where shown.
2. Part numbers with prefix SMBJ, SMCJ, or SMLJ are also available as SMBG, SMCG, or SMLG prefix respectively for gull-wing termination options rather than the J-bend shown.
3. Compliant capabilities include a worst-case 20% tolerance for waveform durations in RTCA/DO-160E.

*PLAD15KPxxx and PLAD30KPxxx series have lower thermal resistance to minimize cumulative heating on multiple surges. This also applies to PLAD18KPxxx and PLAD36KPxxx not yet shown on the table.

**CV suffix signifies lower clamping voltage compared to the CA suffix.

Table 2: Waveform 5A Clamping Voltage and Microsemi TVS Part Numers Compliant to RTCA/DO-160G at 25 °C

P _{pp} @10/1000 μs (or as specified)	LEVEL 1	LEVEL 2	LEVEL 3	LEVEL 4	LEVEL 5
500 W	All SMAJ5.0A-170A,CA P5KE5.0A-170A,CA 1N6103A-6137A 1N6461-1N6468 HSMBJSAC5.0-50 SAC5.0-50	V _C ≤ 10.1 V SMAJ5.0A,CA P5KE5.0A,CA V _C ≥ 114.9 V ** SMAJ78A-170A,CA P5KE78A-170A,CA 1N6130A-6137A	None	None	None
600 W	All SMBJ5.0A-170A,CA P6KE6.8A-200A,CA	V _C ≤ 12.4 V SMBJ5.0A-7.0A,CA P6KE6.8A-8.2A,CA V _C ≥ 112.6 V ** SMBJ75A-170A,CA P6KE91A-200A,CA	None	None	None
1500 W	All SMCJ5.0A-170A,CA 1.5KE6.8A-400A,CA 1N5629A-1N5665A 1N5907, 1N5908 1N6036A-1N6072A 1N6138A-1N6173A 1N6267A-1N6303A 1N6469-1N6476 LC(E)6.5-170A SMCJLCE6.5-170A	V _C ≤ 42.2 V SMCJ5.0A-26A,CA 1.5KE6.8A-30A,CA 1N5629-1N5644A 1N5907, 1N5908 1N6036A-1N6050A 1N6138A-1N6153A 1N6267A-1N6282A 1N6469-1N6473 LC(E)6.5-26A SMCJLCE6.5-26A V _C ≥ 82.8 V ** SMCJ58A-170A,CA 1.5KE68A-400A,CA 1N5653A-1N5665A 1N6059A-1N6072A 1N6162A-1N6173A 1N6291A-1N6303A LC(E)58A-170A SMCJLCE58A-170A	V _C ≤ 12.1 V SMCJ5.0A-7.0A,CA 1.5KE6.8A-8.2A,CA 1N5629-1N5631A 1N5907, 1N5908 1N6036A-1N6037A 1N6138A-1N6140A 1N6267A-1N6269A 1N6469-1N6470 LC(E)6.5-7.0A SMCJLCE6.5-7.0A	None	None
3000 W	All SMLJ5.0A-170A,CA	All SMLJ5.0A-170A,CA	V _C ≤ 25.5 V SMLJ5.0A-15A,CA	V _C ≤ 9.4 V SMLJ5.0A,CA	None
5000 W	All 5KP5.0A-110A,CA	All 5KP5.0A-110A,CA	V _C ≤ 45.8 V 5KP5.0A-28A,CA	V _C ≤ 15.9 V 5KP5.0A-8.0A,CA	None
15,000 W	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA*	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA*	All 15KP17A-280A,CA PLAD15KP5.0-400A,CA*	V _C ≤ 49.9 V 15KP17A-28A,CA PLAD15KP5.0-28A,CA*	V _C ≤ 22.2 V PLAD15KP5.0-13A,CA*
30,000 W	All 30KPA28A-400A,CA PLAD30KP10-400A,CA*	All 30KPA28A-400A,CA PLAD30KP10-400A,CA*	All 30KPA28A-400A,CA PLAD30KP10-400A,CA*	V _C ≤ 109.1 V 30KPA28A-64A,CA PLAD30KP10-64A,CA*	V _C ≤ 45.0 V PLAD30KP10-26A,CA*
65,000 W @ 6.4/69 μs	All RT65KP48-75A,CA	All RT65KP48-75A,CA	All RT65KP48-75A,CA	None	None
100,000 W @ 6.4/69 μs	All RT100KP40-400A,CA	All RT100KP40-400A,CA	All RT100KP40-400A,CA	V _C ≤ 109.1 V RT100KP40-54A,CA	None
130,000 W @ 6.4/69 μs	All RT130KP275-295CA or RT130KP275-295CV***	All RT130KP275-295CA or RT130KP275-295CV***	All RT130KP275-295CA or RT130KP275-295CV***	None	None

1. The CA suffix signifies bidirectional TVS options where shown.
2. Part numbers with prefix SMBJ, SMCJ, or SMLJ are also available as SMBG, SMCG, or SMLG prefix respectively for Gull-wing termination options rather than the J-bend shown.
3. Where no generic standard part is available (none indicated), consult factory for custom options.
4. Compliant capabilities include a worst-case 20% tolerance for waveform durations in RTCA/DO-160E.

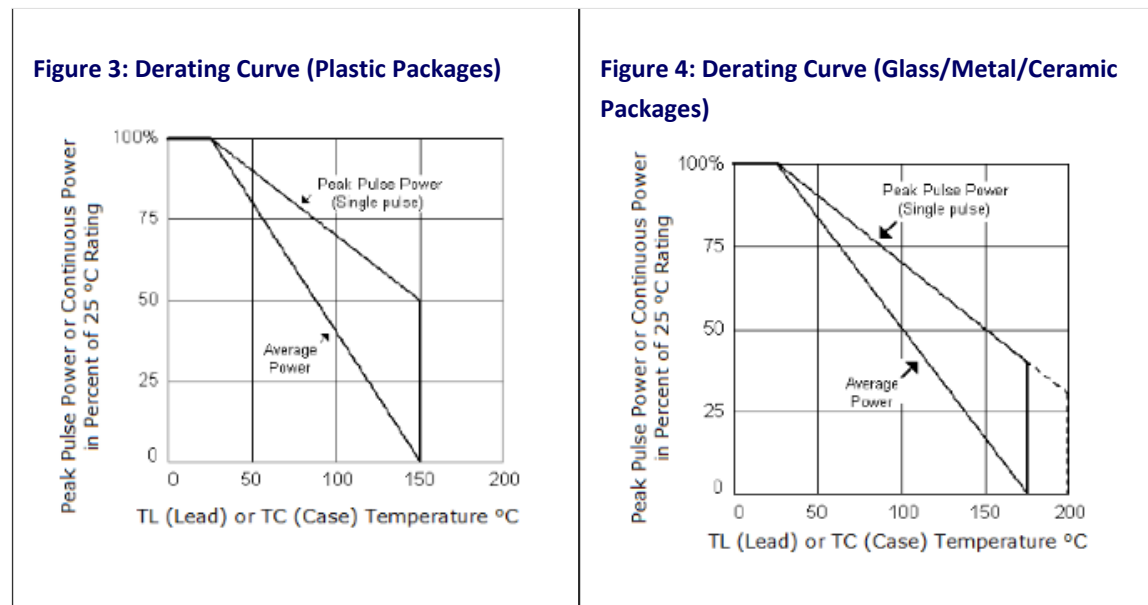
*PLAD15KPxxx and PLAD30KPxxx series have lower thermal resistance to minimize cumulative heating on multiple surges. This also applies to PLAD18KPxxx and PLAD36KPxxx, not yet shown on the table.

**Part numbers are guard banded one higher value to ensure V_C is greater than value shown.

***CV suffix signifies lower clamping voltage compared to the CA suffix.

If higher ambient temperatures are used well beyond the P_{PP} ratings at 25 °C, the product selections in [Table 1 \(see page 4\)](#) and [Table 2 \(see page 5\)](#) will be more limited, as shown in MicroNote 132. For random recurring transient events where a TVS device recovers to ambient temperatures before the next transient, the P_{PP} capability at 25 °C will linearly decline to 50% at 150 °C, as shown in [Figure 3 \(see page 6\)](#). If they are plastic packages and above 150 °C, this linear derating will stop and abruptly become zero. If they are glass, ceramic, or metal packages, they can derate linearly to 175 °C (or 200 °C) as determined by the applicable package material properties and overall ratings in storage and operating temperatures. See [Figure 4 \(see page 6\)](#) for reference.

Most applications are at ambient temperatures well below 150 °C. An operating temperature of 70 °C for TVS devices would derate by 18% from their maximum rating at 25 °C. This would also apply to waveform 4 or 5A transients that are considered single stroke for pin injection test levels. MicroNote 127 further specifies device selection at various elevated temperatures in “DIRECTselect” graphs 1 through 14. The various temperature characterizations in those graphs also show the same selection results at 25 °C in [Table 1 \(see page 4\)](#) and [Table 2 \(see page 5\)](#).



A more severe linear slope derating to zero at 150 °C, 175 °C, or 200 °C is applicable for longer average power considerations, such as when a TVS might also be used as a zener voltage regulator with continuous or dc power. In those examples, the average power derating method becomes applicable as shown in [Figure 3 \(see page 6\)](#) and [Figure 4 \(see page 6\)](#). As a result, there is an important distinction in random recurring transients for P_{PP} (duty factors of 0.01% or less), compared to average long-term power derating considerations. In some applications where multiple stroke or multiple burst surge requirements exist and higher duty factors generate cumulative heating effects, further considerations must be given to minimize those effects. This includes using TVS designs with very low thermal resistance junction to ambient with good heat sinking as may be obtained with the new PLAD15KPxxx and PLAD30KPxxx series products shown in [Table 1 \(see page 4\)](#) and [Table 2 \(see page 5\)](#). This will ensure minimum case temperatures (T_c) for these type packages, as shown in [Figure 3 \(see page 6\)](#) and [Figure 4 \(see page 6\)](#). It will also permit greater P_{PP} performance with the described longer multiple strokes or bursts, which are identified in the RTCA/DO-160 specification.

If the application only involves relatively low current demands for the protected load, external resistance can also be added to the source impedance Z_s , thus reducing the incident surge current level on a TVS protecting that load. This is also described in the first page equations, as well as in MicroNote 125 and MicroNote 127. This will effectively reduce the P_{PP} requirements of the TVS and expand the possible selection of V_c and part numbers provided in [Table 1 \(see page 4\)](#) and [Table 2 \(see page 5\)](#).

In addition to these standard TVS product part numbers, Microsemi also provides options for additional screening where higher reliability testing may be required. For flight hardware, Microsemi offers avionics-grade component screening, available by adding an MA prefix to the standard part number. This screening is performed on 100% of the production units and includes additional surge tests, temperature cycling, and high temperature reverse bias (HTRB) screening. For applications where a militarized device is required and no qualified part exists in accordance with MIL-PRF-19500, Microsemi offers equivalent JAN, JANTX, JANTXV, and JANS screening by adding MQ, MX, MV, or MSP prefixes, respectively, to standard part numbers. This also includes specific options for various low capacitance TVS devices as shown in [Table 1 \(see page 4\)](#) and [Table 2 \(see page 5\)](#) herein. Also see MicroNote 129 for further details on up-screening, where some differences may occur in available options between plastic versus metal versus glass packaging.

In summary, this article has provided a calculation method for transient voltage suppressor compliance to the five test levels of waveform 4 and 5A for pin injection in the RTCA/DO-160G specification and many of its earlier revisions. The results of those calculations are summarized in the tables herein as an overview for providing a quick selection of TVS device part numbers. These same results are also reflected in MicroNote 132 in “DIRECTselect” graphs 1 through 14 at 25 °C, as well as for various elevated temperatures.

Support

For additional technical information, please contact Design Support at:
<http://www.microsemi.com/designsupport>
or Kent Walters (kwalters@microsemi.com) at 480-302-1144


Microsemi Corporate Headquarters

One Enterprise, Aliso Viejo,
 CA 92656 USA
 Within the USA: +1 (800) 713-4113
 Outside the USA: +1 (949) 380-6100
 Fax: +1 (949) 215-4996
 Email: sales.support@microsemi.com
www.microsemi.com

© 2018 Microsemi Corporation. All rights reserved. Microsemi and the Microsemi logo are trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners.

Microsemi makes no warranty, representation, or guarantee regarding the information contained herein or the suitability of its products and service for any particular purpose, nor does Microsemi assume any liability whatsoever arising out of the application or use of any product or circuit. The products sold hereunder and any other products sold by Microsemi have been subject to limited testing and should not be used in conjunction with mission-critical equipment or applications. Any performance specifications are believed to be reliable but are not verified, and Buyer must conduct an complete all performance and other testing of the products, alone and together with, or installed in, any end-products. Buyer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the Buyer's responsibility to independently determine suitability of any products and to test and verify the same. The information provided by Microsemi hereunder is provided "as is, where is" and with all faults, and the entire risk associated with such information is entirely with the Buyer. Microsemi does not grant, explicitly or implicitly, to any party any patent rights licenses, or any other IP rights, whether with regard to such information itself or anything described by such information. Information provided in this document is proprietary to Microsemi, and Microsemi reserves the right to make any changes to the information in this document or to any product and services at any time without notice.

Microsemi Corporation (Nasdaq: MSCC) offers a comprehensive portfolio of semiconductor and system solutions for aerospace & defense, communications, data center and industrial markets. Products include high-performance and radiation-hardened analog mixed-signal integrated circuits, FPGAs, SoCs and ASICs; power management products; timing and synchronization devices and precise time solutions, setting the world standard for time; voice processing devices; RF solutions; discrete components; enterprise storage and communication solutions; security technologies and scalable anti-tamper products; Ethernet solutions; Power-over-Ethernet ICs and midspans; as well as custom design capabilities and services. Microsemi is headquartered in Aliso Viejo, California, and has approximately 4,800 employees globally. Learn more at www.microsemi.com.